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Title: Method and Arrangement for....
Inventor: Jaakko VIHRIALA Page # 1 of 7

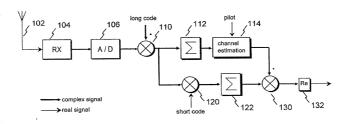


FIG. 1 PRIOR ART

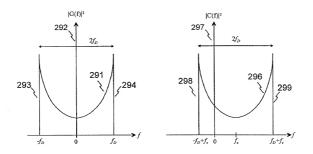


FIG. 2a

FIG. 2b

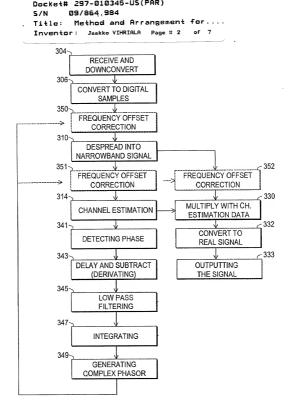


FIG. 3

Method and Arrangement for . . . Title: Inventor: Jaakko VIHRIALA Page # 3 404 RECEIVE AND DOWNCONVERT 406~ 1 CONVERT TO DIGITAL SAMPLES 410~ DESPREAD INTO NARROWBAND SIGNAL 430 414~ MULTIPLY WITH CH. 1. CHANNEL ESTIMATION ESTIMATION DATA 431 441~ SUMMING TO OTHER DETECTING PHASE INITIAL DECISIONS - 432 443~ DELAY AND SUBTRACT CONVERT TO (DERIVATING) REAL SIGNAL - 434 445~ PERFORMING SUMMING TO OTHER A DECISION CONTROL VARIABLES ? 449~ **GENERATING** COMPLEX PHASOR 451-- 452 MULTIPLY MULTIPLY 453~ SUMMING 454-2. CHANNEL ESTIMATION 460~ SIGNAL CORRECTION 461~ COMBINING WITH OTHER FINAL DECISIONS 462~ CONVERT TO REAL SIGNAL 463~ FIG. 4 OUTPUTTING THE SIGNAL

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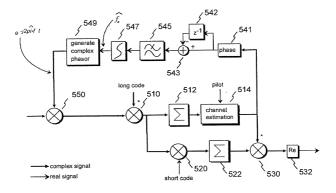


FIG. 5

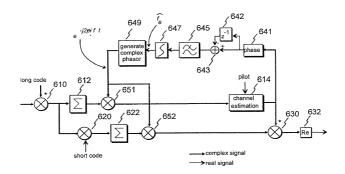
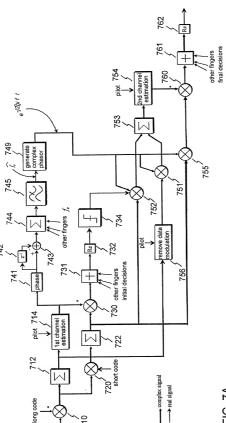


FIG. 6

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:IG. 7A

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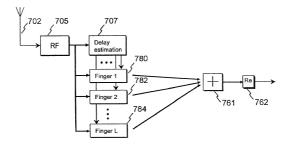


FIG. 7B

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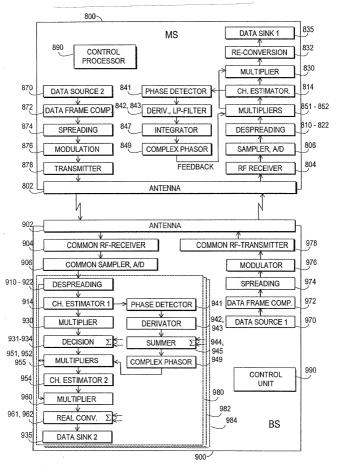


FIG. 8